AMENDMENTS TO CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A pixel circuit for liquid crystal display for lowering power consumption via combining an analogue and a digital circuit, the circuit comprises:

a plurality of multiplexers, acting as switching elements for performing a plurality of output voltage transforming functions[[.]], said plurality of multiplexers comprising a first multiplexer and a second multiplexer;

a thin film transistor, for connecting a scanning line and a data line, acting as a control switch of the circuit; and

a capacitor, connecting to the thin film transistor, where analogue or digital signals from the data line are stored,

wherein said second multiplexer further comprises:

a selection terminal;

an output terminal;

a first mode terminal; and

a second mode terminal.

2. (Canceled)

3. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~2~~ 1, wherein said first multiplexer further comprises a general voltage terminal and a reference voltage terminal.

4. (Canceled)

5. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~4~~ 1, wherein said second multiplexer further connects to a mode terminal.

6. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~4~~ 1, wherein said output terminal further connects to a liquid crystal unit.

7. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~4~~ 1, wherein said first mode terminal further connects to the capacitor and the thin film transistor.

8. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~4~~ 1, wherein the second mode terminal further connects to the output terminal of the first multiplexer.

9. (Currently Amended) A pixel circuit for liquid crystal display for lowering power consumption via combining an analogue and a digital circuit, the circuit comprises:

a plurality of multiplexers, acting as switching elements for performing a plurality of output voltage transforming functions[[.]], said plurality of multiplexers comprising a first multiplexer and a second multiplexer;

a thin film transistor, for connecting a scanning line and a data line, acting as a control switch of the circuit; and

a capacitor, connecting to the thin film transistor, where analogue or digital signals from the data line are stored; and

a first switch device, for connecting said plurality of multiplexer and a liquid crystal unit,

wherein said second multiplexer further comprises:

a selection terminal;

an output terminal;

a first mode terminal; and

a second mode terminal.

10. (Canceled)

11. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~10~~ 9, wherein said first multiplexer further comprises a general voltage terminal and a reference voltage terminal.

12. (Canceled)

13. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~12~~ 9, wherein said second multiplexer further connects to a mode terminal.

14. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~12~~ 9, wherein the output terminal further connects to the first switch device.

15. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~12~~ 9, wherein the first mode terminal further connects to the liquid crystal unit.

16. (Currently Amended) The pixel circuit for liquid crystal display of claim ~~12~~ 9, wherein the second mode terminal further connects to the first multiplexer.